

Electronic Acknowledgement Receipt

EFS ID:	7007810
Application Number:	10564473
International Application Number:	
Confirmation Number:	9263
Title of Invention:	Delay time correction circuit, video data processing circuit, and flat display device
First Named Inventor/Applicant Name:	Masaki Murase
Customer Number:	23353
Filer:	Brian Keith Dutton
Filer Authorized By:	
Attorney Docket Number:	SON-3058
Receipt Date:	12-FEB-2010
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Transmittal Letter	DC379569.pdf	16644 cd417e2dce372831725bd65b1683921eb9f217cf	no	1

Warnings:**Information:**

2	Appeal Brief Filed	DC379429.pdf	132477 4b93459c7faf0ab3f7ba6beb189429dacc96c11d	no	27
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Warnings:**Information:**

3	Fee Worksheet (PTO-875)	fee-info.pdf	30001 c6ba1f28919437a62bd084c175a9053b4a722977	no	2
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Total Files Size (in bytes):			179122
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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